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7590 12/20/2005

Jon W. Hallman
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Saundra L. Clark	(Depositor's name)
Saundra L. Clark	(Signature)
3-7-06	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/660,814	09/12/2003	Satwant Singh	M-15198 US	7052

TITLE OF INVENTION: DELAY-MATCHED ASIC CONVERSION OF A PROGRAMMABLE LOGIC DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional	NO	\$1400	\$0	\$1400	03/20/2006
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EXAMINER	ART UNIT	CLASS-SUBCLASS
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CHANG, DANIEL D	2819	326-041000
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1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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 "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list
 (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
 (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1. Jonathan W. Hallman
MacPherson Kwok
2. Chen & Heid
3. _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE **Lattice Semiconductor Corporation** (B) RESIDENCE: (CITY and STATE OR COUNTRY) **Hillsboro, Oregon**

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are enclosed:

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A check in the amount of the fee(s) is enclosed.
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 The Director is hereby authorized to charge the required fee(s), or credit any overpayment, to Deposit Account Number 50-2252 (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Jon W. HallmanDate 3/7/06Typed or printed name Jonathan W. HallmanRegistration No. 42,1922

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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MAR 07 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Satwant Singh		
Assignee:	Lattice Semiconductor Corporation		
Title:	Delay-Matched ASIC Conversion Of A Programmable Logic Device		
Serial No.:	10/660,814	Filing Date:	September 12, 2003
Examiner:	Daniel D. Chang	Group Art Unit:	2819
Docket No.:	M-15198 US	Confirmation No.	7052

Irvine, California
March 6, 2006

Fax No.: 1-571-273-2885

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I hereby certify that the following documents are being facsimile transmitted to the Patent and Trademark Office on the date shown below.

- 1) Part B – Fee(s) Transmittal
- 2) “Fee Address” Indication Form
- 3) Change of Correspondence Address

Dated: 3-7-06By: Saundra L. Carr
Saundra L. CarrTotal Pages Being Transmitted: 4

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